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## **U.S. UTILITY Patent Application**

**PATENT NUMBER**

<b>O.I.P.E.</b> <b>MH</b> SCANNED <u>AC<sup>2</sup></u> on <u>AA</u>	<b>PATENT DATE</b>
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APPLICATION NO. 09/942962	CONT/PRIOR F	CLASS 438	SUBCLASS 107	ART UNIT 2012 2787	EXAMINER Zarate, L.
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Sasaki Takaaki

1. Semiconductor package for three-dimensional mounting, fabrication method thereof, and semiconductor device

PTD-2040  
12/99

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED		
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE		
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER		
	(Assistant Examiner)	(Date)	(Primary Examiner)	(Date)	(Legal Instruments Examiner)	(Date)

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